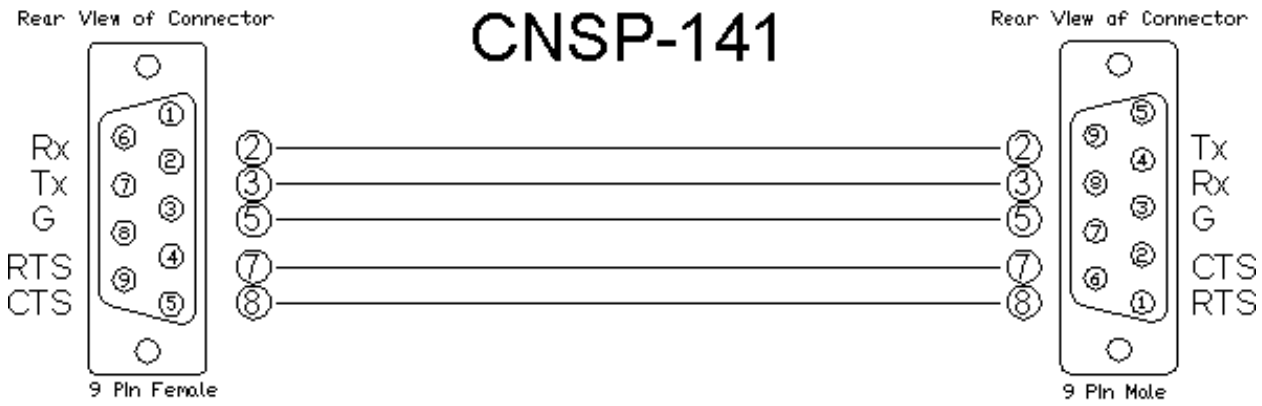
Partner: ClearOne
Model: Converge
Device Type: Conferencing

GENERAL INFORMATION

SIMPLWINDOWS NAME:	ClearOne Converge Feedback Processor v1.7
CATEGORY:	Conferencing
VERSION:	1.7
SUMMARY:	Processes all feedback received from the ClearOne Converge Series products.
GENERAL NOTES:	This module will process all feedback received from the ClearOne Converge series units. It will reformat the information and then send it out to as many other ClearOne Converge modules are in the program. In a properly constructed program, there will be one copy of this module receiving all of the information from the ClearOne system. The output of this module will be connected to the FROM-CLEARONE-PROCESSOR$ input of all other ClearOne Converge modules in the program.
CRESTRON HARDWARE REQUIRED:	CNX-COM2, ST-COM, 2-Series Processor, C2COM3
SETUP OF CRESTRON HARDWARE:	RS232 Baud: 57600 Parity: N Data Bits: 8 Stop Bits: 1 RTS/CTS Handshaking should be enabled to insure no data is lost.
VENDOR FIRMWARE:	4.0.0.2.4
VENDOR SETUP:	Flow control should be set to "on". The baud rate should be set to 57600.
CABLE DIAGRAM:	CNSP-141



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CONTROL:

From_Device$	S	Connect to the com port to process all information received from the ClearOne system.

FEEDBACK:

FROM-CLEARONE-PROCESSOR$	S	Connect to all FROM-CLEARONE-PROCESSOR$ inputs on all other ClearOne Converge modules in the program.

TESTING:

OPS USED FOR TESTING:	PRO2 v4.007.0004 CP3 v1.008.0040
SIMPL WINDOWS USED FOR TESTING:	v4.02.38.00
DEVICE DB USED FOR TESTING:	v55.00.002.00
CRES DB USED FOR TESTING:	v44.05.005.00
SYMBOL LIBRARY USED FOR TESTING:	V508
SAMPLE PROGRAM:	ClearOne Converge Series Demo v1.7 PRO2.smw ClearOne Converge Series Demo v1.7 CP3.smw
REVISION HISTORY:	v1.1 v1.2 – Resolved issue with adding Type-ID's A, D, and G. v1.3 – Revised to work with added Type-ID-HEX parameter in ClearOne Converge modules. v1.4 – Added support for ClearOne Converge SR1212 Matrix Routing module. Will also now support the 880TA and SR1212A module. v1.5 – Added support for ClearOne Converge VH20. v1.7 – Added support for later model 3-Series processors and matched all revisions to v1.7

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